

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	199882	(detect\$3 or correct\$3 or test\$3) with (peak or LOS or loss-to-signal or (loss near5 (signal or data)) or (condition near5 (signal or data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 18:59
L2	441539	(detect\$3 or correct\$3 or test\$3) near5 (failure or condition)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 16:58
L3	107699	(signal adj strength) or (threshold adj level)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 17:01
L4	568736	1 or 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 16:59
L5	25293	3 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 17:00
L6	3490	5 and (sampl\$3 near5 rate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 17:00
L7	1107	6 and (stream near5 (bit or data))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 17:01
L8	12654	3 near5 compar\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 17:04

L9	158	7 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 17:04
L10	125	@ad<"20011212" and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 19:02
L12	16052	(detect\$3 or correct\$3 or test\$3) near5 (LOS or loss-to-signal or (loss near5 (signal or data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 19:01
L14	151	3 same 12	USPAT	OR	OFF	2005/08/11 19:02
L15	137	@ad<"20011212" and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 19:02

Day : Thursday
Date: 8/11/2005

Time: 17:06:34

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = STEINER

First Name = PHILIP

Application#	Patent#	Status	Date Filed	Title	Inventor Name
60569187	Not Issued	159	05/07/2004	VIDEO GAME PLATFORM AND USER INTERFACE	STEINER, PHILIPP
60403545	Not Issued	159	08/14/2002	CANCER TREATMENT AGENTS AND METHODS OF USE	STEINER, PHILIPP W.
60302914	Not Issued	159	07/03/2001	CALIBRATION OF A LOSS OF SIGNAL DETECTION SYSTEM	STEINER, PHILIP DAVID
60302912	Not Issued	159	07/03/2001	METHOD AND APPARATUS FOR DETERMINING A LOSS OF SIGNAL CONDITION	STEINER, PHILIP DAVID
60302902	Not Issued	159	07/03/2001	USE OF DUAL HYSTERESIS MODES IN DETERMINING A LOSS OF SIGNAL OUTPUT INDICATION	STEINER, PHILIP DAVID
60252201	Not Issued	159	11/20/2000	SOFT SUPERLATTICE UNDERLAYER FOR PERPENDICULAR RECORDING MEDIA	STEINER, PHILIP
60172505	Not Issued	159	12/17/1999	MODULAR ELECTRIC CONDUCTOR AND SYSTEM	STEINER, PHILIPP
60068840	Not Issued	159	12/24/1997	THE PROMOTER OF THE CATALYTIC SUBUNIT OF HUMAN TELOMERASE	STEINER, PHILIPP W.
29147783	D468194	150	09/05/2001	SPRAYER HEAD	STEINER, PHILIPP
29147778	D454779	150	09/05/2001	LIQUID SPRAY CONTAINER	STEINER, PHILIPP
29147776	D455352	150	09/05/2001	CONTAINER	STEINER, PHILIPP
29147771	D454778	150	09/05/2001	SPRAYER HEAD	STEINER, PHILIPP

<u>29147770</u>	<u>D454504</u>	150	09/05/2001	CONTAINER	STEINER, PHILIPP
<u>29147756</u>	Not Issued	161	09/05/2001	GROOVES FOR A LIQUID SPRAY CONTAINER	STEINER, PHILIPP
<u>29125524</u>	<u>D451792</u>	150	06/26/2000	LIQUID SPRAY CONTAINER	STEINER, PHILIPP
<u>10640964</u>	Not Issued	160	08/13/2003	CANCER TREATMENT AGENTS AND METHODS OF USE	STEINER, PHILIPP W.
<u>10380417</u>	Not Issued	071	09/11/2003	GASEOUS ACID CATALYSIS	STEINER, PHILIPP D
<u>10293678</u>	<u>6797885</u>	150	11/13/2002	MODULAR ELECTRIC CONDUCTOR	STEINER, PHILIPP
<u>10015030</u>	Not Issued	030	12/11/2001	METHOD AND APPARATUS FOR DETERMINING A LOSS OF SIGNAL CONDITION	STEINER, PHILIP DAVID
<u>09958936</u>	<u>6642396</u>	150	01/14/2002	PROCESS FOR THE PRODUCTION OF FURFURAL FROM LIGNOSULPHONATE WASTE LIQUOR	STEINER, PHILIPP DANIEL
<u>09956052</u>	<u>6641935</u>	150	09/20/2001	PERPENDICULAR RECORDING MEDIA WITH SOFT MAGNETIC SUPERLATTICE UNDERLAYER	STEINER, PHILIP
<u>09952629</u>	Not Issued	030	09/11/2001	IN-FLIGHT ENTERTAINMENT PASSENGER CONTROL UNIT	STEINER, PHILIPP
<u>09949493</u>	<u>6799131</u>	150	09/07/2001	CALIBRATION OF A LOSS OF SIGNAL DETECTION SYSTEM	STEINER, PHILIP DAVID
<u>09949483</u>	<u>6489803</u>	150	09/07/2001	USE OF DUAL HYSTERESIS MODES IN DETERMINING A LOSS OF SIGNAL OUTPUT INDICATION	STEINER, PHILIP DAVID
<u>09906916</u>	<u>6642879</u>	150	07/16/2001	METHOD AND SYSTEM FOR POWERING DOWN AN ANALOG-TO-DIGITAL CONVERTER INTO A SLEEP MODE	STEINER, PHILIP
<u>09715816</u>	<u>6492594</u>	150	11/17/2000	MODULAR ELECTRIC CONDUCTOR AND SYSTEM	STEINER, PHILIPP
<u>08838056</u>	<u>5838272</u>	150	04/17/1997	ERROR CORRECTING SIGMA-DELTA MODULATION DECODING	STEINER, PHILIP
<u>07850443</u>	<u>5291355</u>	250	03/10/1992	MICRO MINIATURE HARD	STEINER, PHILIP

				DISK DRIVE	L.
<u>07223702</u>	Not Issued	161	07/21/1988	MAGNETIC DISK WITH A HIGH INCIDENCE CHROMIUM UNDERLAYER	STEINER, PHILIP L.
<u>06936882</u>	Not Issued	166	12/02/1986	MAGNETIC DISK WITH A HIGH INCIDENCE CHROMIUM UNDERLAYER	STEINER, PHILIP L.

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="STEINER"/>	<input type="text" value="PHILIP"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 8/11/2005

Time: 17:06:45

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = PERROTT

First Name = MICHAEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>60570309</u>	Not Issued	159	05/12/2004	SYNCHRONIZATION OF LASERS AND RF-SOURCES USING TIMING INFORMATION TRANSFER IN THE OPTICAL DOMAIN	PERROTT, MICHAEL
<u>60302935</u>	Not Issued	159	07/03/2001	OFFSET CORRECTION AND SLICING LEVEL ADJUSTMENT FOR AMPLIFIER CIRCUITS	PERROTT, MICHAEL H.
<u>60302932</u>	Not Issued	159	07/03/2001	AUTO-DETECTION BETWEEN REFERENCELESS AND REFERENCE CLOCK MODE OF OPERATION	PERROTT, MICHAEL H.
<u>60302914</u>	Not Issued	159	07/03/2001	CALIBRATION OF A LOSS OF SIGNAL DETECTION SYSTEM	PERROTT, MICHAEL H.
<u>60302912</u>	Not Issued	159	07/03/2001	METHOD AND APPARATUS FOR DETERMINING A LOSS OF SIGNAL CONDITION	PERROTT, MICHAEL H.
<u>60300699</u>	Not Issued	159	06/25/2001	FEEDBACK SYSTEM INCORPORATING SLOW DIGITAL SWITCHING FOR GLITCH-FREE STATE CHANGES	PERROTT, MICHAEL H.
<u>60278784</u>	Not Issued	159	03/26/2001	CLOCK AND DATA RECOVERY CIRCUIT WITHOUT JITTER PEAKING	PERROTT, MICHAEL H.
<u>60217208</u>	Not Issued	159	07/10/2000	DIGITALLY-SYNTHESIZED LOOP FILTER CAPACITOR	PERROTT, MICHAEL H.
<u>60217207</u>	Not Issued	159	07/10/2000	AUTO-DETECTION OF AN EXTERNALLY PROVIDED REFERENCE CLOCK FREQUENCY	PERROTT, MICHAEL H.

<u>60135937</u>	Not Issued	159	05/25/1999	LIPOSOME-ASSISTED SYNTHESIS OF POLYMERIC NANOPARTICLES	PERROTT, MICHAEL G.
<u>60061805</u>	Not Issued	159	10/14/1997	MOLECULAR COMPOUNDS HAVING COMPLEMENTARY SURFACES TO TARGETS AND METHODS FOR THE SYNTHESIS AND USE THEREOF	PERROTT, MICHAEL G.
<u>11125800</u>	Not Issued	019	05/10/2005	SYNCHRONIZATION OF LASERS AND RF SOURCES USING TIMING INFORMATION TRANSFER IN THE OPTICAL DOMAIN	PERROTT, MICHAEL
<u>11057466</u>	Not Issued	030	02/14/2005	METHOD AND APPARATUS FOR ACQUIRING A FREQUENCY WITHOUT A REFERENCE CLOCK	PERROTT, MICHAEL
<u>10895002</u>	Not Issued	030	07/20/2004	DIGITALLY-SYNTHESIZED LOOP FILTER METHOD AND CIRCUIT PARTICULARLY USEFUL FOR A PHASE LOCKED LOOP	PERROTT, MICHAEL H.
<u>10701732</u>	Not Issued	030	11/05/2003	METHOD AND APPARATUS FOR REDUCING QUANTIZATION NOISE IN FRACTIONAL-N FREQUENCY SYNTHESIZERS	PERROTT, MICHAEL HENDERSON
<u>10656425</u>	<u>6765445</u>	150	09/05/2003	DIGITALLY-SYNTHESIZED LOOP FILTER CIRCUIT PARTICULARLY USEFUL FOR A PHASE LOCKED LOOP	PERROTT, MICHAEL H.
<u>10373640</u>	Not Issued	041	02/24/2003	METHODS FOR PREPARING POLYMERIC COMPLEXES OF PHARMACEUTICALLY ACTIVE AGENTS	PERROTT REPPY, MICHAEL G.
<u>10187935</u>	<u>6741109</u>	150	07/02/2002	METHOD AND APPARATUS FOR SWITCHING BETWEEN INPUT CLOCKS IN A PHASE- LOCKED LOOP	PERROTT, MICHAEL H.
<u>10180833</u>	Not Issued	030	06/25/2002	FEEDBACK SYSTEM INCORPORATING SLOW DIGITAL SWITCHING FOR GLITCH-FREE STATE CHANGES	PERROTT, MICHAEL H.
<u>10131002</u>	<u>6831523</u>	150	04/24/2002	AUTO-DETECTION BETWEEN REFERENCELESS AND	PERROTT, MICHAEL H.

				REFERENCE CLOCK MODE OF OPERATION	
<u>10055837</u>	<u>6884842</u>	150	10/26/2001	MOLECULAR COMPOUNDS HAVING COMPLEMENTARY SURFACES TO TARGETS	PERROTT, MICHAEL G.
<u>10036986</u>	<u>6657488</u>	150	12/31/2001	OFFSET CORRECTION AND SLICING LEVEL ADJUSTMENT FOR AMPLIFIER CIRCUITS	PERROTT, MICHAEL H.
<u>10015030</u>	Not Issued	030	12/11/2001	METHOD AND APPARATUS FOR DETERMINING A LOSS OF SIGNAL CONDITION	PERROTT, MICHAEL H.
<u>09949493</u>	<u>6799131</u>	150	09/07/2001	CALIBRATION OF A LOSS OF SIGNAL DETECTION SYSTEM	PERROTT, MICHAEL H.
<u>09902548</u>	<u>6580376</u>	150	07/10/2001	APPARATUS AND METHOD FOR DECIMATING A DIGITAL INPUT SIGNAL	PERROTT, MICHAEL H.
<u>09902543</u>	<u>6686803</u>	150	07/10/2001	INTEGRATED CIRCUIT INCORPORATING CIRCUITRY FOR DETERMINING WHICH OF AT LEAST TWO POSSIBLE FREQUENCIES IS PRESENT ON AN EXTERNALLY PROVIDED REFERENCE SIGNAL AND METHOD THEREFOR	PERROTT, MICHAEL H.
<u>09902542</u>	<u>6590426</u>	150	07/10/2001	DIGITAL PHASE DETECTOR CIRCUIT AND METHOD THEREFOR	PERROTT, MICHAEL H.
<u>09902541</u>	<u>6630868</u>	150	07/10/2001	DIGITALLY-SYNTHESIZED LOOP FILTER CIRCUIT PARTICULARLY USEFUL FOR A PHASE LOCKED LOOP	PERROTT, MICHAEL H.
<u>09888717</u>	Not Issued	041	06/25/2001	CLOCK AND DATA RECOVERY CIRCUIT WITHOUT JITTER PEAKING	PERROTT, MICHAEL
<u>09888708</u>	Not Issued	092	06/25/2001	METHOD AND APPARATUS FOR BIT ERROR RATE DETECTION	PERROTT, MICHAEL H.
<u>09888663</u>	<u>6856206</u>	150	06/25/2001	METHOD AND APPARATUS FOR ACQUIRING A FREQUENCY WITHOUT A REFERENCE CLOCK	PERROTT, MICHAEL H.
<u>09579860</u>	<u>6217901</u>	150	05/25/2000	LIPOSOME-ASSISTED SYNTHESIS OF POLYMERIC	PERROTT, MICHAEL G.

				NANOPARTICLES	
<u>09291924</u>	Not Issued	161	04/14/1999	COMPLEXES OF PHARMACEUTICALLY ACTIVE AGENTS USEFUL FOR IN VIVO DELIVERY	PERROTT, MICHAEL G.
<u>09172921</u>	Not Issued	161	10/14/1998	MOLECULAR COMPOUNDS HAVING COMPLEMENTARY SURFACES TO TARGETS AND METHODS FOR THE SYNTHESIS AND USE THEREOF	PERROTT, MICHAEL G.
<u>08791215</u>	<u>6008703</u>	150	01/31/1997	DIGITAL COMPENSATION FOR WIDEBAND MODULATION OF A PHASE LOCKED LOOP FREQUENCY SYNTHESIZER	PERROTT, MICHAEL H.
<u>07756371</u>	<u>D342787</u>	150	09/09/1991	DIAPER	PERROTTE, MICHAEL J.
<u>07627350</u>	Not Issued	161	12/14/1990	REUSABLE AND WASHABLE DIAPER	PERROTTE, MICHAEL J.

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	PERROTT	MICHAEL	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 8/11/2005

Time: 17:07:07

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = GUTNIK

First Name = VADIM

Application#	Patent#	Status	Date Filed	Title	Inventor Name
60667435	Not Issued	020	03/31/2005	REDUNDANT NON-VOLATILE MEMORY CELL	GUTNIK, VADIM
60667180	Not Issued	020	03/30/2005	RFID TAGS GENERATING RNS BASED ON NOISE	GUTNIK, VADIM
60667131	Not Issued	020	03/30/2005	RFID TAGS UPDATING SEED OF PRNG	GUTNIK, VADIM
60396500	Not Issued	159	07/17/2002	RING OSCILLATOR FOR USE IN PARALLEL SAMPLING OF HIGH SPEED DATA	GUTNIK, VADIM
60396499	Not Issued	159	07/17/2002	SLICE VOLTAGE COMPENSATION	GUTNIK, VADIM
60302912	Not Issued	159	07/03/2001	METHOD AND APPARATUS FOR DETERMINING A LOSS OF SIGNAL CONDITION	GUTNIK, VADIM
60221709	Not Issued	159	07/31/2000	ACTIVE GHZ CLOCK NETWORK USING DISTRIBUTED PHASE LOCKED LOOPS	GUTNIK, VADIM
11106982	Not Issued	020	04/15/2005	REDUNDANT NON-VOLATILE MEMORY CELL	GUTNIK, VADIM
11016546	Not Issued	030	12/17/2004	RFID TAGS WITH ELECTRONIC FUSES FOR STORING COMPONENT CONFIGURATION DATA	GUTNIK, VADIM
11015293	Not Issued	030	12/17/2004	RFID TAGS STORING COMPONENT CONFIGURATION DATA IN NON-VOLATILE MEMORY AND METHODS	GUTNIK, VADIM
10824073	Not Issued	020	04/13/2004	METHOD AND SYSTEM TO CALIBRATE AN OSCILLATOR WITHIN AN RFID CIRCUIT BY SELECTING A CALIBRATION VALUE FROM A PLURALITY	GUTNIK, VADIM

				OF STORED CALIBRATION VALUES	
<u>10824071</u>	Not Issued	020	04/13/2004	METHOD AND SYSTEM TO CALIBRATE AN OSCILLATOR WITHIN AN RFID CIRCUIT UTILIZING A TEST SIGNAL SUPPLIED TO THE RFID CIRCUIT	GUTNIK, VADIM
<u>10824070</u>	Not Issued	030	04/13/2004	METHOD AND SYSTEM TO CALIBRATE AN OSCILLATOR WITHIN AN RFID CIRCUIT RESPONSIVE TO A RECEIVED UPDATE VALUE	GUTNIK, VADIM
<u>10824048</u>	Not Issued	030	04/13/2004	METHOD AND SYSTEM TO GENERATE MODULATOR AND DEMODULATOR CLOCK SIGNALS WITHIN AN RFID CIRCUIT UTILIZING A MULTI-OSCILLATOR ARCHITECTURE	GUTNIK, VADIM
<u>10823977</u>	Not Issued	030	04/13/2004	METHOD AND SYSTEM TO BACKSCATTER MODULATE A RADIO-FREQUENCY SIGNAL FROM AN RFID TAG IN ACCORDANCE WITH BOTH AN OSCILLATION FREQUENCY SIGNAL AND A COMMAND SIGNAL	GUTNIK, VADIM
<u>10303422</u>	Not Issued	030	11/25/2002	RING OSCILLATOR FOR USE IN PARALLEL SAMPLING OF HIGH SPEED DATA	GUTNIK, VADIM
<u>10303398</u>	Not Issued	071	11/25/2002	SLICE VOLTAGE COMPENSATION	GUTNIK, VADIM
<u>10015030</u>	Not Issued	030	12/11/2001	METHOD AND APPARATUS FOR DETERMINING A LOSS OF SIGNAL CONDITION	GUTNIK, VADIM
<u>09919372</u>	Not Issued	041	07/31/2001	CLOCK DISTRIBUTION CIRCUITS AND METHODS OF OPERATING SAME THAT USE MULTIPLE CLOCK CIRCUITS CONNECTED BY PHASE DETECTOR CIRCUITS TO GENERATE AND SYNCHRONIZE LOCAL CLOCK SIGNALS	GUTNIK, VADIM
<u>09478106</u>	<u>6661860</u>	150	01/04/2000	MULTIPLE ARBITER JITTER ESTIMATION SYSTEM AND RELATED TECHNIQUES	GUTNIK, VADIM

08210199	5839062	150	03/18/1994	MIXING, MODULATION AND DEMODULATION VIA ELECTROMECHANICAL RESONATORS	GUTNIK, VADIM
----------	---------	-----	------------	---	---------------

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
GUTNIK	VADIM	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)